The right chip

eSilicon: A unique design and engineering methodology for optimized, first-time-right custom ICs, in volume.

www.esilicon.com or contact us at info@esilicon.com

For ASICs or ASSPs, custom IP to optimize your chip’s performance, or semiconductor manufacturing services to drive your proven chip through volume production, eSilicon is the right partner.

Since 2000, eSilicon has specialized in designing and manufacturing ASICs for our customers: system OEMs and fabless semiconductor companies.

Strike the right balance
Custom IC design isn’t simply about getting the highest performance anymore. It’s about getting the target performance at the optimized power and area, on time, on budget, and into volume production for faster time to revenue.

Choose the right partner
eSilicon supports custom IC design and manufacturing with three solutions groups and best-in-class supply chain partners that include the top companies in design services, design tools, semiconductor IP, manufacturing, test and package/assembly.

Our ASIC Solutions Group manages ASIC design and production. We use in-house modelling expertise to help you choose the ideal third-party or custom IP for your design. Then we manage your ASIC through test and yield to predictable, on-time device delivery in volume.

Our Semiconductor Manufacturing Services (SMS) Solutions Group can manage your tested, production-ready chips through volume production.

Our IP Solutions Group supports both the ASIC and SMS groups, as well as independent standard and custom IP engagements with customers. We supply an array of 28nm-180nm silicon-proven customizable memories, including ternary and binary content-addressable memory (TCAM, BCAM), SRAM, cache, ROM, asynchronous register files and multi-port register files (MPRF).

Get the chip right
The permutations of technologies, variants, libraries, and IP are increasing exponentially for complex custom ICs. eSilicon’s unique methodology allows for a scientific exploration of a broad set of possible solutions based on 12 years of experience and over 230 custom SoC designs.

Custom memories
With embedded memory often occupying more than 50 percent of a typical SoC, custom memories can optimize a design for power, performance or area to deliver the right chip at the right price for your market.

Reduced risk and time
From design through production, eSilicon manages the elements that impact volume silicon, including design for test (DFT), design for yield (DFY) and design for manufacture (DFM).

Our patented eSilicon Access® production management system is a unique web...
interface that delivers instant access to critical work-in-process (WIP), supplier, materials and status information so you can view the production status of your ASIC 24/7.

**Lower total cost of ownership**
Many of our customers pay less for their devices now than they paid when they performed all operations functions internally. As a value chain producer (VCP), eSilicon earns margin by reducing inefficiencies in the supply chain to deliver a lower total cost of ownership. Our advanced design and test methodologies—DFT, DFY and DFM—combined with our skilled product and test engineering teams, deliver additional savings.

**Flexibility**
We can explore trade-offs in all dimensions (NRE/ASP, die/package, standard IP/custom IP) to help determine the right design solution for your chip.

Flexibility also means you can engage with us at any point in the design process: RTL, Netlist, GDSII, or proven chip, then ramp to volume production, as shown in the diagram below.

**Global company, local support**
Whether you need access to our top engineering talent or a local interface to our supply chain partners, eSilicon is nearby.

**Locations:**
- **Headquarters:** Silicon Valley
- **APAC:** Shanghai, China; Seoul, Korea; Singapore; Taipei City, Taiwan; Ho Chi Minh City and Da Nang, Vietnam
- **EMEA:** Bucharest and Iasi, Romania; Barcelona, Spain
- **North America:** Murray Hill, New Jersey; Allentown, Pennsylvania

**From design through volume production, we get your chip right.**